

FIG. 1

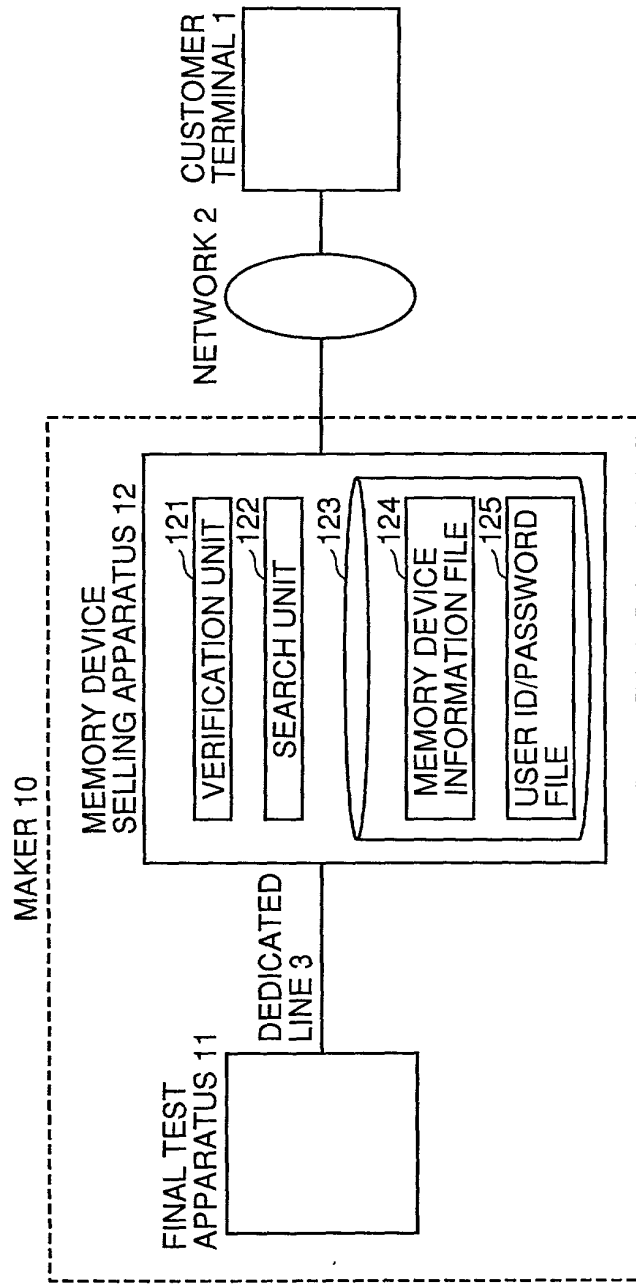


FIG.2

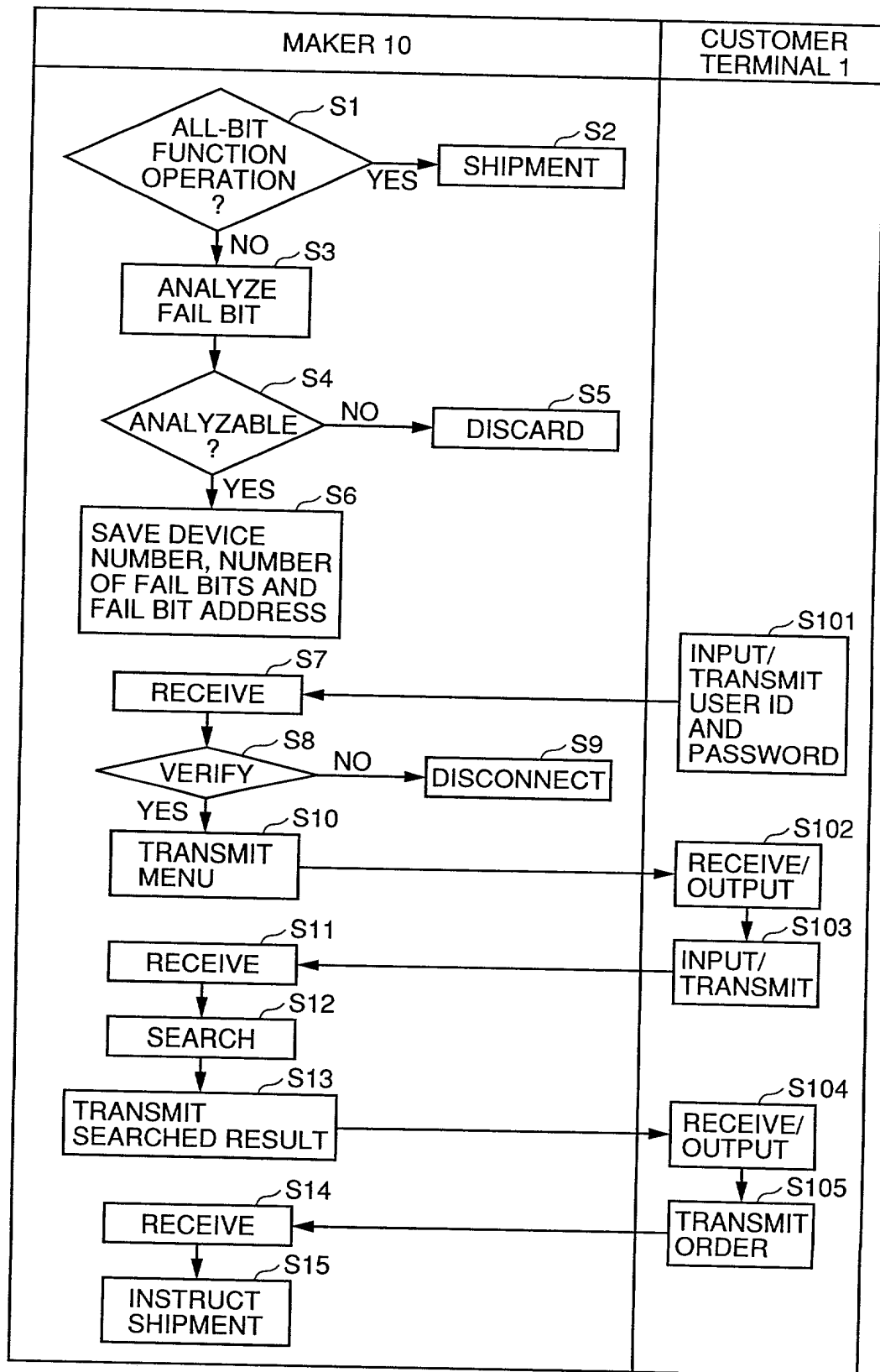


FIG.3

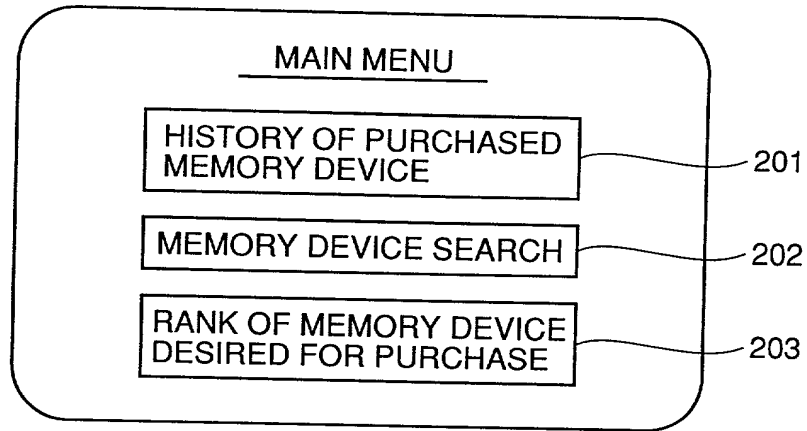


FIG.4

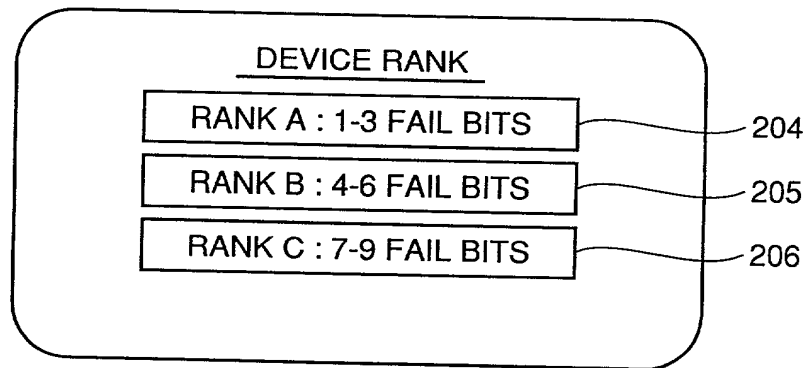


FIG.5

DATA OF RANK A						
DATE CODE	LOT NUMBER	DEVICE NUMBER	NUMBER OF FAIL BITS	FAIL BIT ADDRESS		PURCHASE REQUEST
000515	TSZ440	F4AAB003	2	(A45C,07C5)	(A32D,03D5)	—
000515	TSZ440	F4AAB011	1	(C41Y,02F6)	—	—
000626	TTA010	F5ABB178	1	(B25C,01V3)	—	—
000626	TTA010	F5ABB179	3	(D03R,05E4)	(S24E,11T5)	(V05W,95J1)
...
...
...
...

FIG.6

LIST OF PURCHASED MEMORY DEVICES	
	DATE CODE:000515
	DATE CODE:000723
	DATE CODE:001010

FIG.7

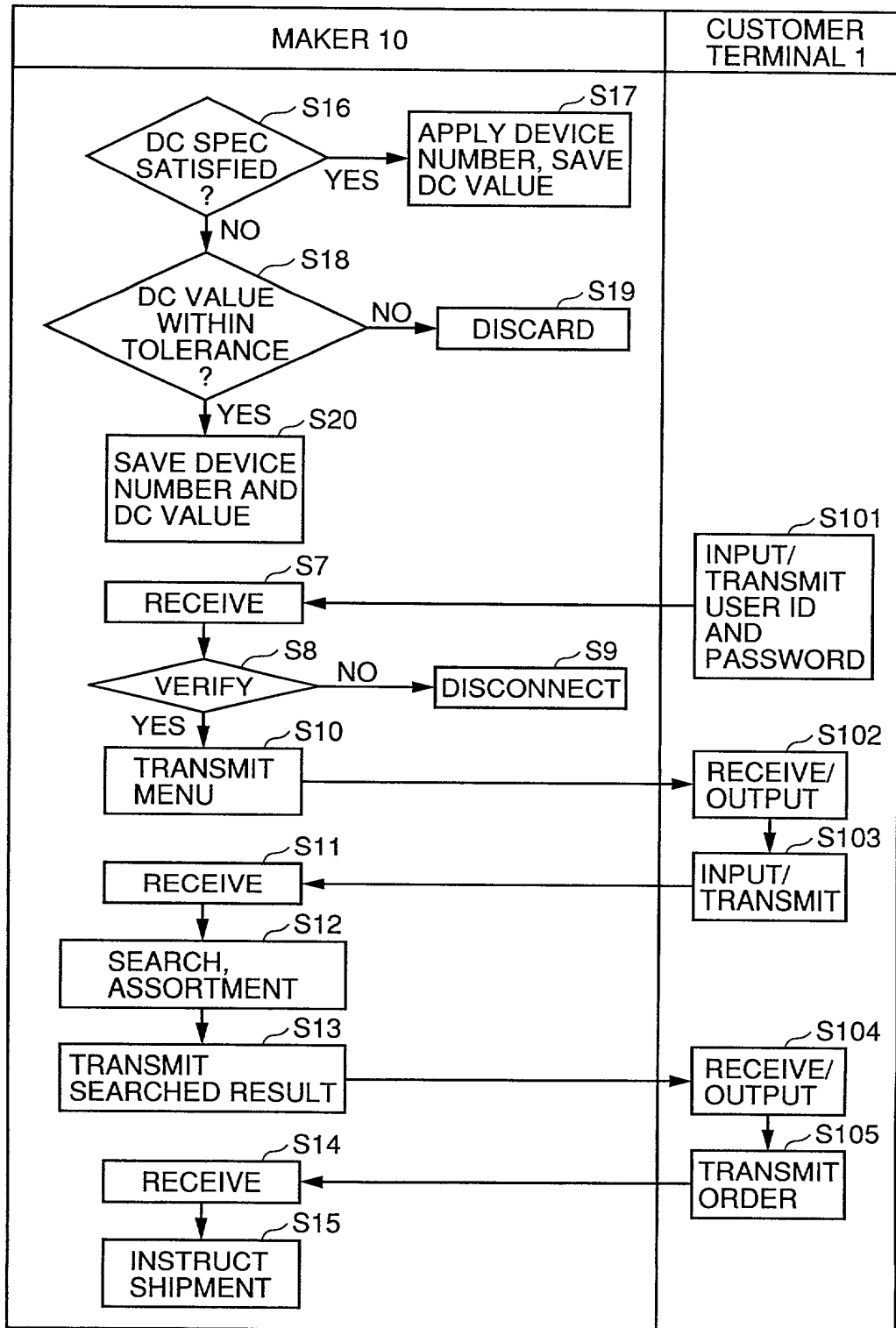


FIG.8

CONDITION OF DEVICE DESIRED FOR PURCHASE		
NUMBER OF DEVICES USED	DEVICES/SETS	207
UPPER LIMIT OF CURRENT VALUE	mA	208
NUMBER OF SETS DESIRED FOR PURCHASE	SETS	209
NUMBER OF SEMIDEFECTIVE DEVICES/SETS	DEVICES/SETS	210

FIG.9

CONDITION OF DEVICE DESIRED FOR PURCHASE	
NUMBER OF DEVICES USED	10DEVICES/SETS
UPPER LIMIT OF CURRENT VALUE	10mA
NUMBER OF SETS DESIRED FOR PURCHASE	100SETS
NUMBER OF SEMIDEFECTIVE DEVICES/SETS	5DEVICES/SETS

FIG.10

THE 100TH SET			
THE FIRST SET			
NO	DEVICE NUMBER	DETERMINATION	DC VALUE
1	F4AAB003	NON-DEFECTIVE	0.9
2	F4AAB001	SEMIDEFECTIVE	1.1
3	J7KLN005	SEMIDEFECTIVE	1.2
4	J7KLN058	NON-DEFECTIVE	0.8
5	P5IJY251	NON-DEFECTIVE	0.7
6	P5IJY252	SEMIDEFECTIVE	1.3
7	P5IJY253	NON-DEFECTIVE	0.9
8	...	NON-DEFECTIVE	0.8
9	...	SEMIDEFECTIVE	1.1
10	...	SEMIDEFECTIVE	1.2
TOTAL NUMBER		TOTAL NUMBER OF SEMIDEFECTIVES	TOTAL DC VALUE
10 DEVICES/SETS		5 DEVICES/SETS	10 mA

ORDER 211